

BALASORE SCHOOL OF ENGINEERING, BALASORE

Department of Electronics & Telecommunication Engineering

LESSON PLAN FOR 3rd SEMESTER, ETC/COMP.SC

SUBJECT- DIGITAL ELECTRONICS (Th-03)

NAME OF THE FACULTY- MISS JASMINE NESSA/HARISH CH SAHU

SL NO	MONTH	DATE	CH. NO	TOPICS TO BE COVERED	NO. OF PERIODS REQUIRED AS PER SYLLABUS	NO OF PERIODS AVAILAB LE AS PER PLAN				
	sep	15/09/22 19/09/22	1	Unit-1: Basics of Digital Electronics 1.1 Number System-Binary, Octal, Decimal, Hexadecimal - Conversion from one system to another number system.	12	12				
		20/09/22 21/09/22		1.2 Arithmetic Operation-Addition, Subtraction, Multiplication, Division, 1"s & 2"s complement of Binary numbers& Subtraction using complements method						
		22/09/22 24/09/22		1.3 Digital Code & its application & distinguish between weighted & non-weight Code, Binary codes, excess-3 and Gray codes.						
		26/09/22 27/09/22		1.4 Logic gates: AND,OR,NOT,NAND,NOR, Exclusive-OR, Exclusive-NOR--Symbol, Function, expression, truth table & timing diagram						
		28/09/22		1.5 Universal Gates& its Realisation						
		29/09/22		1.6 Boolean algebra, Boolean expressions, Demorgan"s Theorems.						
		30/09/22		1.7 Represent Logic Expression: SOP & POS forms						
		1/10/22 10/10/22 11/10/22		1.8 Karnaugh map (3 & 4 Variables)&Minimization of logical expressions ,don"t care conditions Unit						
		oct		12/10/22 13/10/22			2	Unit-2: Combinational logic circuits	12	12
				15/10/22				2.1 Half adder, Full adder		
	17/10/22 18/10/22		2.1 Half Subtractor, Full Subtractor							
	19/10/22 20/10/22		2.1 Serial and Parallel Binary 4 bit adder							
	22/10/22 25/10/22		2.2 Multiplexer (4:1), De- multiplexer (1:4)							
			2.2 Decoder, Encoder							



42	20/12/22 21/12/22	6.3 Features, circuit operation & various applications of TTL(NAND),		
43	22/12/22	6.3 CMOS (NAND & NOR)		

SL NO	MONTH	CHAPTERS TO BE COMPLETED	% COMPLETED
1	SEP	CH1 (-1.1-1.7)	25
2	OCT	CH 1.8 CH 2.2	25
3	NOV	CH 2.3 CH 3 CH 4	30
4	DEC	CH 5 CH6	20

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